	Туре	Ŀ#	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
1	BRS	L3	224	"counter" with "programmable memory"	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:07			0
2	BRS	L4	0	13 and ("EEPROM" with "two memory")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 09:56			0
3	BRS	L5	Ο	13 and ("EEPROM" same "two memory")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 09:56			0
4	BRS	L6	35	13 and "EEPROM"	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 09:56			0
5	BRS	<b>L</b> 7	2	16 and (memory adj (area\$1 or region\$1))	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:07			0

	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
6	BRS	L8	2	5384749.pn.	JPO; JPO; DERW ENT; IBM_	2004/08/ 05 09:59			0
7	BRS	L10	1	17 and bit\$1	TDB USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:08			0
8	BRS	L11	0	17 and bit\$1 and updat\$3	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:10			0
9	BRS	L12	0	lll and bit\$1 and updat\$3	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:25			0
10	BRS	L13	0	110 and updat\$3	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:10			0

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comment	Error Definition	Er ro
11	BRS	L14	8	16 and updat\$3	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM TDB	2004/08/ 05 10:10			0
12	BRS	L15	153	((EPROM\$1 or eeprom\$1 or (flash adj Eprom\$1)) with counter) and (memory adj (area\$1 or region\$1))	USPA T; US-P GPUB ;	2004/08/ 05 10:59			0
13	BRS	L17	12	l16 and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:04	Rev'd kwic/i mages		0
14	BRS	L18	İ	l16 and (valid with ((cyclic adj redundancy) or "CR" or "SCR"))	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:42			0
15	BRS	L19	1	l17 and ("backup" or "back-up" or "back up")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 10:55			0

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
16	BRS	L20	6	(4887234, 5687398, "5434999").pn.		2004/08/ 05 10:58			0
17	BRS	L21	0	120 and (EEPROM with counter)	USPA T; US-P GPUB	2004/08/ 05 10:59			0
18	BRS	L22	0	120 and ((EPROM\$1 or eeprom\$1 or (flash adj Eprom\$1)) with counter) and (memory adj (area\$1 or region\$1))	GPUB ;	2004/08/ 05 10:59			Ο

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	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs	
19	BRS	L23	2	115 and ((predetermined or predefined) adj bit\$1)	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 11:04			0	
20	BRS	L16	54	115 and bit\$1 and updat\$3	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	i .	Rev'd kwic/i mages		0	
21	BRS	L24	17	("3869700"   "4529870"   "4746786" "4780602" "4864109" "4900903" "4930129" "5012076" "5120939" "5120939" "5264689" "5266782" "5359182" "5428684" "5479637" "5495098"   "5644118").PN.	USPA T	2004/08/ 05 12:36			0	
22	BRS	L25	13	5914471.URPN.	USPA T	2004/08/ 05 13:39			0	
23	BRS	L30	4	125 and counter	USPA T	2004/08/ 05 13:43			0	
24	BRS	L31	3	125 and counter and updat\$3	Т	2004/08/ 05 13:43			0	
25	BRS	L32	9	124 and counter	Т	2004/08/ 05 13:43			0	
26	BRS	L33	6	132 and counter and updat\$3	Т	05 13:58			0	
27	BRS	L34	0	<pre>133 and ((redundancy adj check) or CRC)</pre>	T	2004/08/ 05 13:59			0	
28	BRS	L35	1	l15 and ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:07			0	

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	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	ro rs
29	BRS	L36	3276	memory and ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:07		·	0
30	BRS	L37	776	memory with ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:07			0
31	BRS	L38	0	"programmable memory" with ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:08			0
32	BRS	L39	2	"programmable memory" same ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:10			0
33	BRS	L40	0	("programmable memory" with ("back-up" or "backup" or (back adj up))) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:10	1		0

	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
34	BRS	L41	1132	integrity and ("back-up" or "backup" or (back adj up)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:19			0
35	BRS	L42	22	l41 and (programmable adj memory)	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:11	Rev'd kwic/i mages		0
36	BRS	L43	15	141 and (programmable adj memory) and counter	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB				0
37	BRS	L44	1161	integrity and ("back-up" or "backup" or (back adj up) or "fail over" or (stand adj by)) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:16			Ο
38	BRS	L45	107	integrity and  (("back-up" or  "backup" or (back adj up)) adj copy) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:20			О

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		Type	L#	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Er ro rs
	39	BRS	L46	36	integrity and (("back-up" or "backup" or (back adj up)) adj memory) and ((cyclic adj redundancy) or "CR" or "SCR")	USPA T; US-P GPUB; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/ 05 14:30	Rev'd kwic/i mages		0